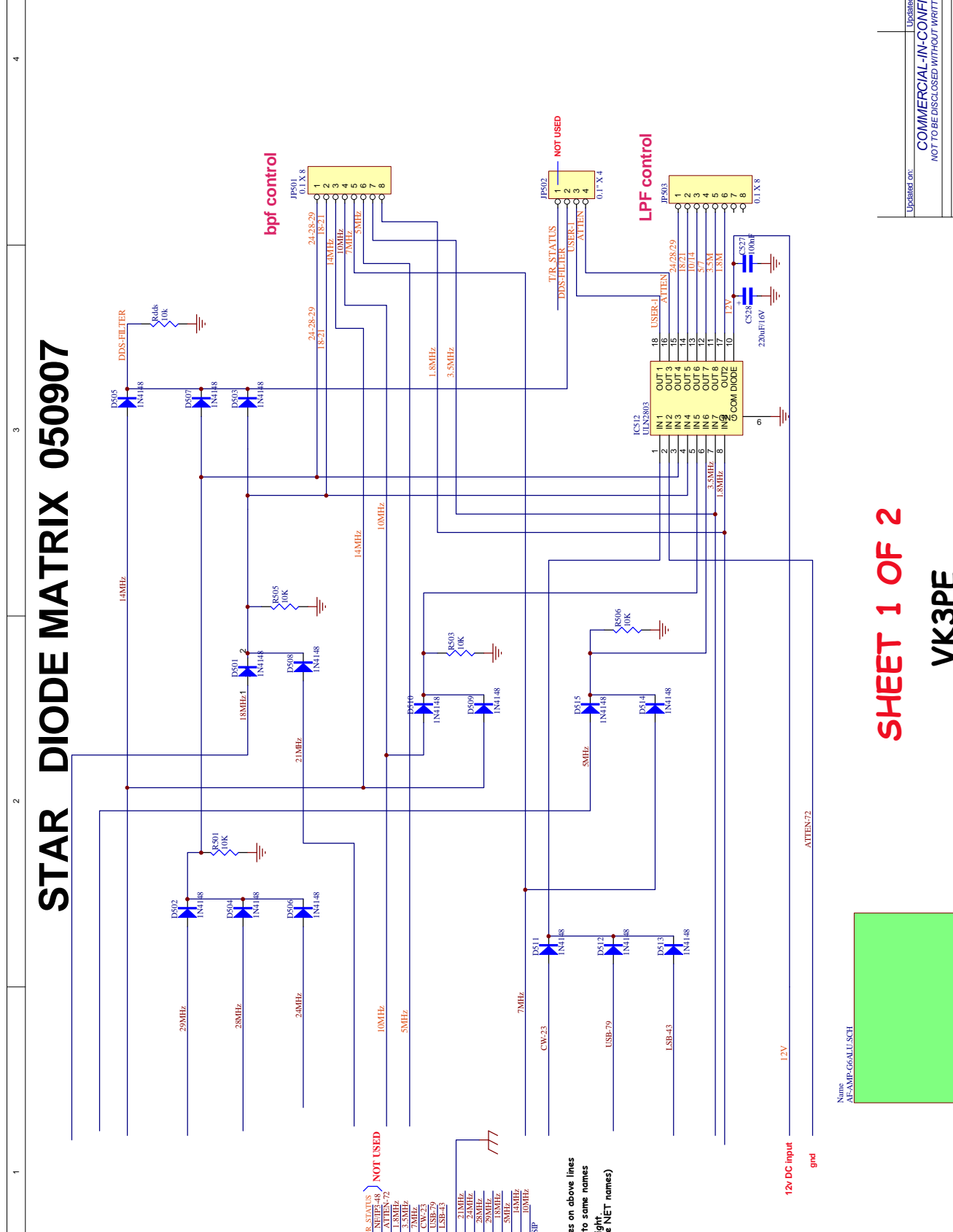


STAR DIODE MATRIX 050907



- JP501 0.1 X 8
- 1 TR STATUS
 - 2 NOT USED
 - 3 USER-1
 - 4 USER-2
 - 5 USER-3
 - 6 USER-4
 - 7 USER-5
 - 8 USER-6
 - 9 USER-7
 - 10 USER-8
 - 11 2.1MHz
 - 12 2.4MHz
 - 13 2.8MHz
 - 14 3.5MHz
 - 15 4.8MHz
 - 16 5MHz
 - 17 10MHz
 - 18 10MHz
- IC512 ULN2803
- 1 IN1
 - 2 IN2
 - 3 IN3
 - 4 IN4
 - 5 IN5
 - 6 IN6
 - 7 IN7
 - 8 IN8
 - 9 COM DIODE
 - 10 12V
 - 11 1.8MHz
 - 12 3.5MHz
 - 13 5MHz
 - 14 7MHz
 - 15 10MHz
 - 16 14MHz
 - 17 18MHz
 - 18 21MHz
 - 19 24MHz
 - 20 28MHz
 - 21 20MHz
- JP502 TR STATUS DDS-FILTER
- 1 USER-1
 - 2 USER-2
 - 3 USER-3
 - 4 USER-4
- JP503 LPF control
- 1 OUT1
 - 2 OUT2
 - 3 OUT3
 - 4 OUT4
 - 5 OUT5
 - 6 OUT6
 - 7 OUT7
 - 8 OUT8
 - 9 1.8MHz
 - 10 3.5MHz
 - 11 5MHz
 - 12 7MHz
 - 13 10MHz
 - 14 14MHz
 - 15 18MHz
 - 16 21MHz
 - 17 24MHz
 - 18 28MHz
 - 19 20MHz
- DS01-DS06, DS11-DS13 IN4148
- R501, R503, R505, R506 10K
- C527, C528 100nF, 220nF10V
- 12V DC input, gnd

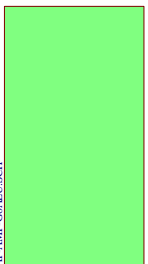
Note:
the names on above lines
connect to some names
to the right.
(they are NET names)

SHEET 1 OF 2

VK3PE

ORIGINAL SCHEMATIC BY G6ALU

Name: AF-AMP-G6ALU.SCH



Updated on:	Updated by:
COMMERCIAL-IN-CONFIDENCE NOT TO BE DISCLOSED WITHOUT WRITTEN AUTHORITY	
Title:	Issue:
PICNMIX-DIODE-MATRIX.SCH	DRAFT
Size:	Number:
A3	
Printed on: 27-Oct-2007 13:00:41	Sheet: 4 of 4
File: G:\Pnl-2208\Diode Logic Schemas\MATRIX_AF-AMP_VA_050907-PROJ.SCH	Proj: PROJ.SCH

